

Slater & Matsil, L.L.P.

Suite 1000
17950 Preston Road
Dallas, Texas 75252-5793
Phone: 972-732-1001 Facsimile: 972-732-9218

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To: Examiner Monica Lewis	Total Pages Sent: 2
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Facsimile Number: (571) 273-8300	Transmission Date: December 21, 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE


Applicant: Thomas, *et al.* Docket No.: 2003 P 54322 US
Serial No: 10/790,907 Art Unit: 2822
Date Filed: March 2, 2004
Title: Integrated Circuit with Re-Route Layer and Stacked Die Assembly

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- Certification of Facsimile Transmission (1 page)
- Election (1 page)

Respectfully submitted,


Kristin R. Hayes
Legal Assistant

Confirmation Respectfully Requested

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Serial No.: 10/790,907 Art Unit: 2822
Filed: March 2, 2004 Examiner: Lewis, Monica
For: Integrated Circuit with Re-Route Layer and Stacked Die Assembly

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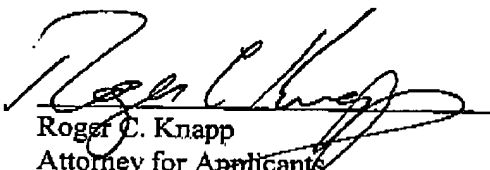
ELECTION

Dear Sir:

In response to the restriction requirement set forth in the Office Action mailed on November 30, 2005, Applicants hereby elect without traverse Group II, Claims 53-91, for further prosecution in the above patent application.

Respectfully submitted,

December 21, 2005
Date


Roger C. Knapp
Attorney for Applicants
Reg. No. 46,836

SLATER & MATSIL, L.L.P.
17950 Preston Rd.
Suite 1000
Dallas, TX 75252-5793
972-732-1001 - Tel
972-732-9218 - Fax